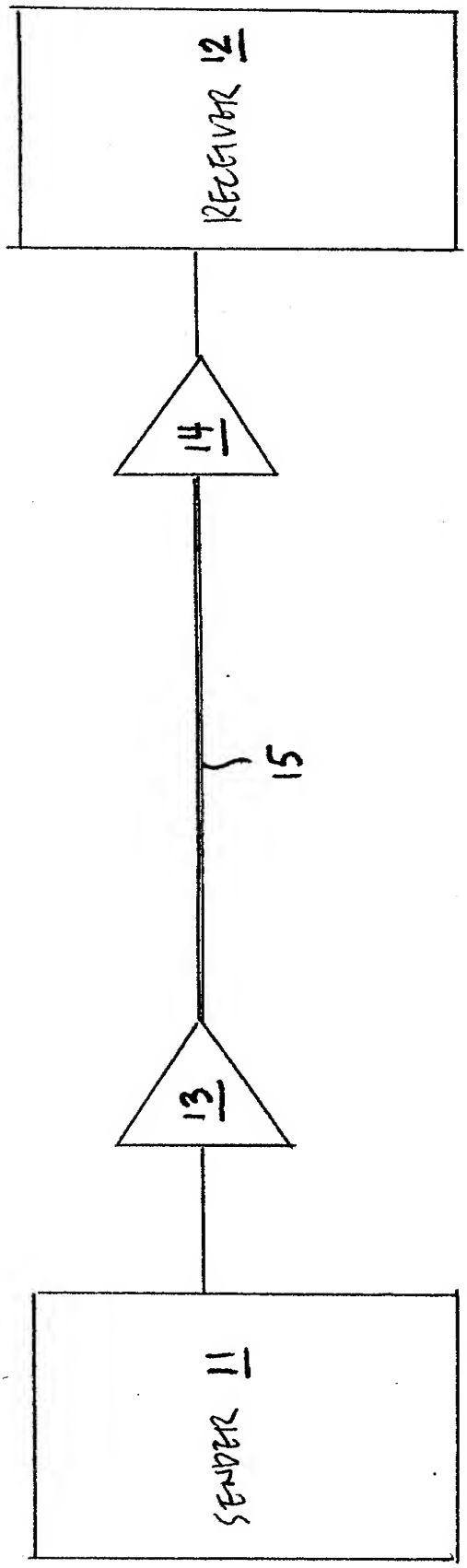

FIGURE 1
(PRIOR ART)

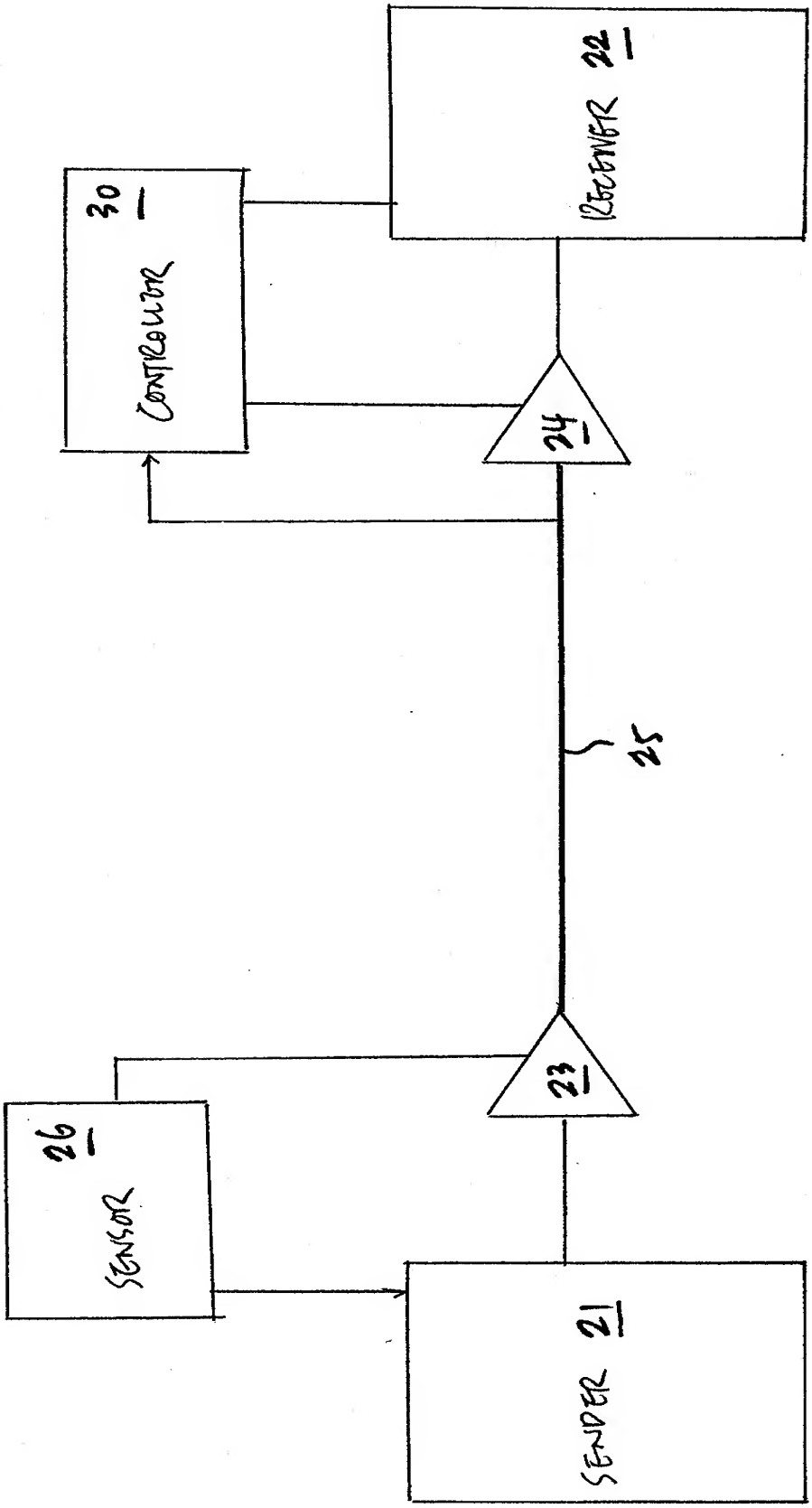

FIGURE 2

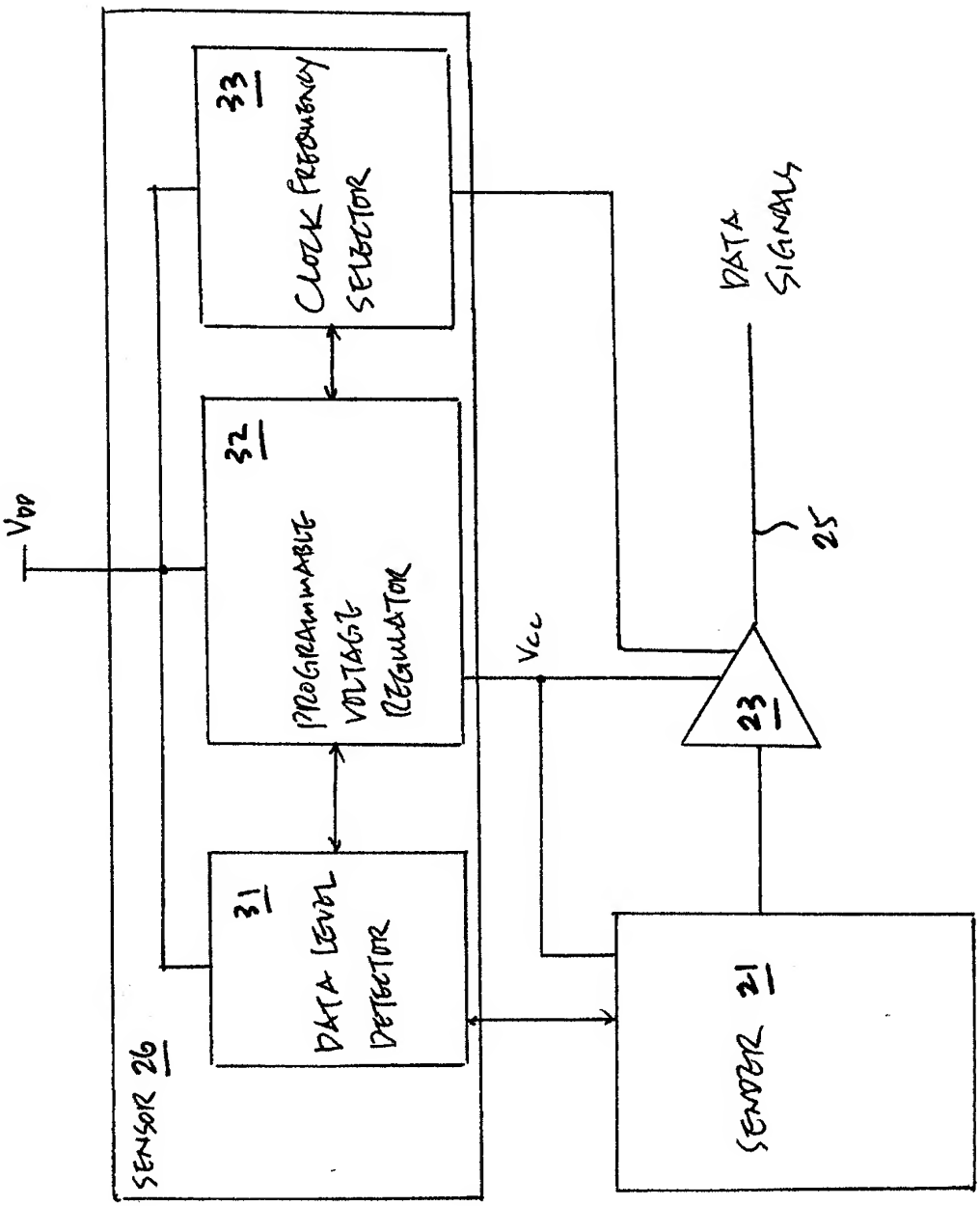
SENSOR 26
31
DATA LEVEL DETECTOR
32
PROGRAMMABLE VOLTAGE REGULATOR
33
CLOCK FREQUENCY SELECTOR
V_{DD}
V_{CC}
DATA SIGNALS
25
23
SENSOR 21

FIGURE 3

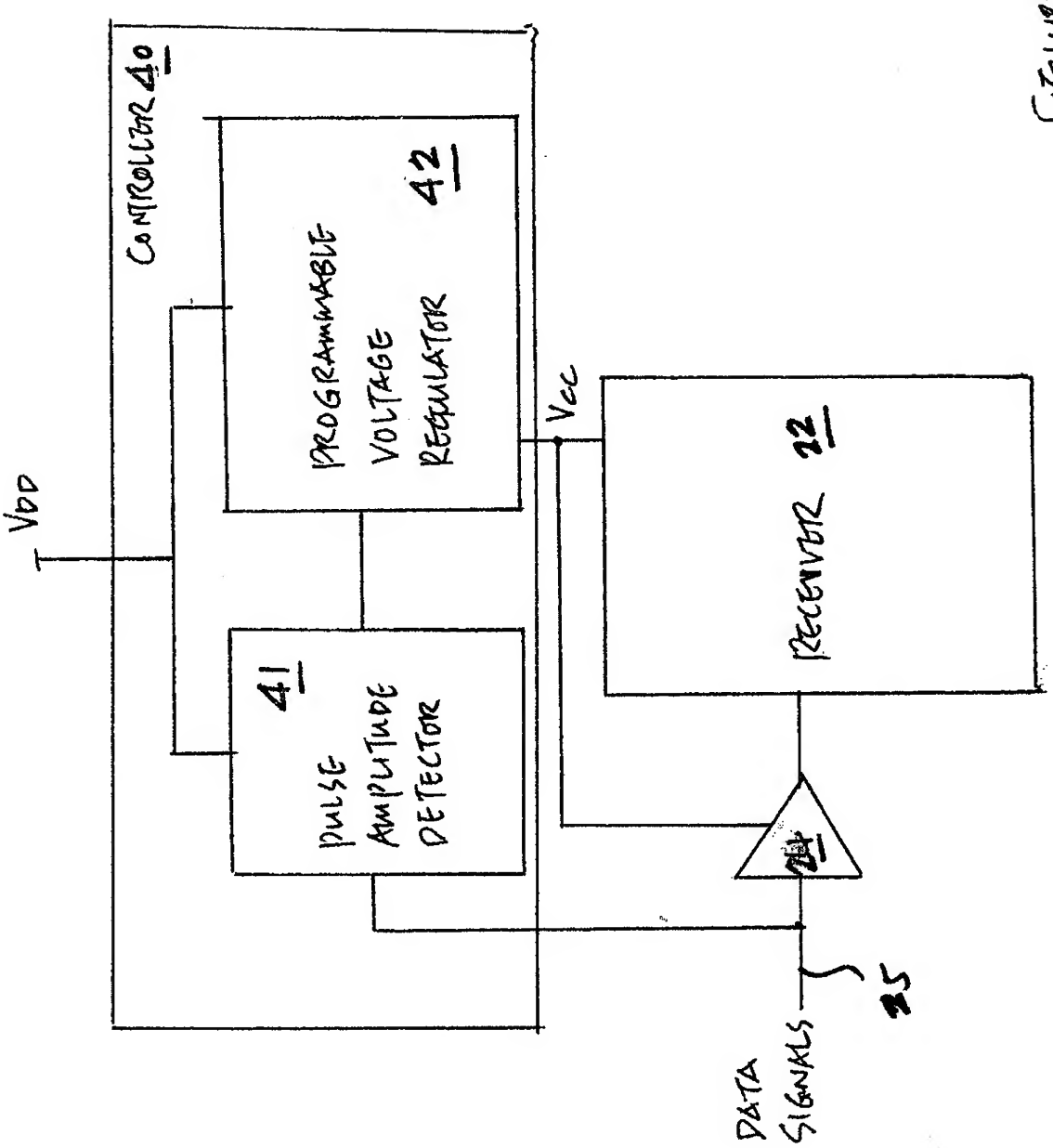
CONTROLLER 40
PROGRAMMABLE VOLTAGE REGULATOR 42
PULSE AMPLITUDE DETECTOR 41
VDD
Vcc
RECEIVER 12
DATA SIGNALS
25

FIGURE 4